

5 é / Descriptions

9 5 : . > // x J· 6 4 6 â o Silicon PNP Digital transistor in a SOT-23 Plastic Package.

α a / Features

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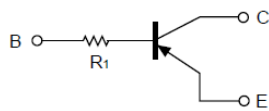
With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process,HF Product.

Đ ÷ / Applications

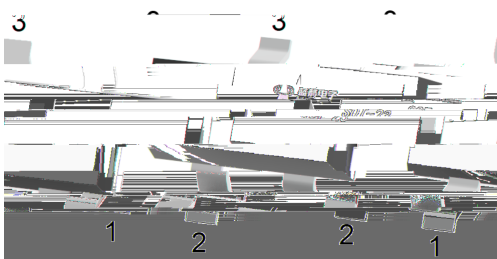
9 ...o F , Ło μ Ł Y C Ü Ł o

Switching, inverter circuit, interface circuit and driver circuit applications.

Ã W] Ô · / Equivalent Circuit



• Ũ - æ / Pinning



PIN 1 y Base

PIN 2 y Emitter

PIN 3 y Collector

Z Ü B , M V / h_{FE} Classifications & Marking

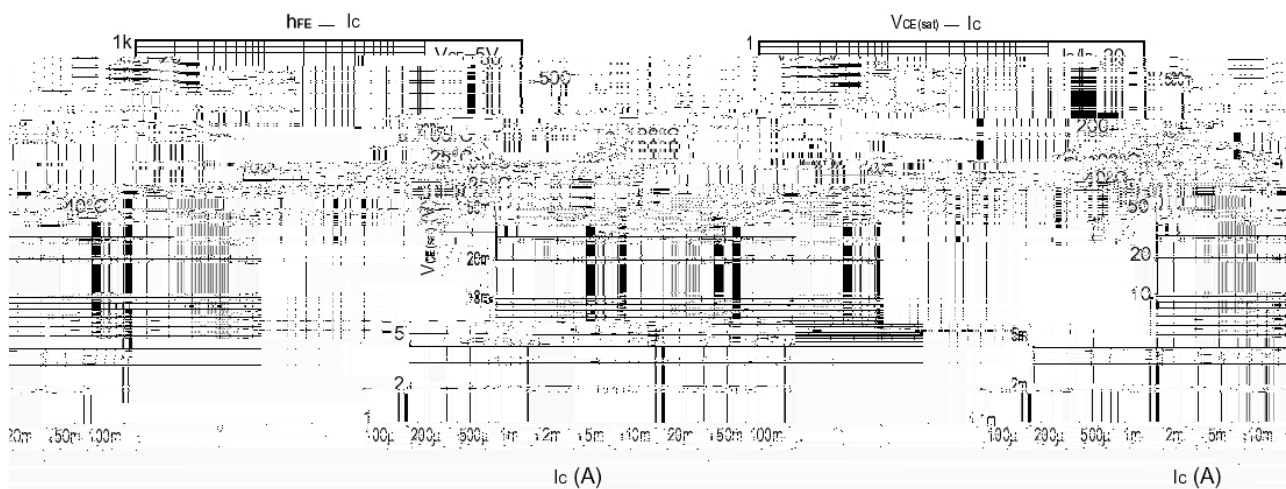
Marking

H93

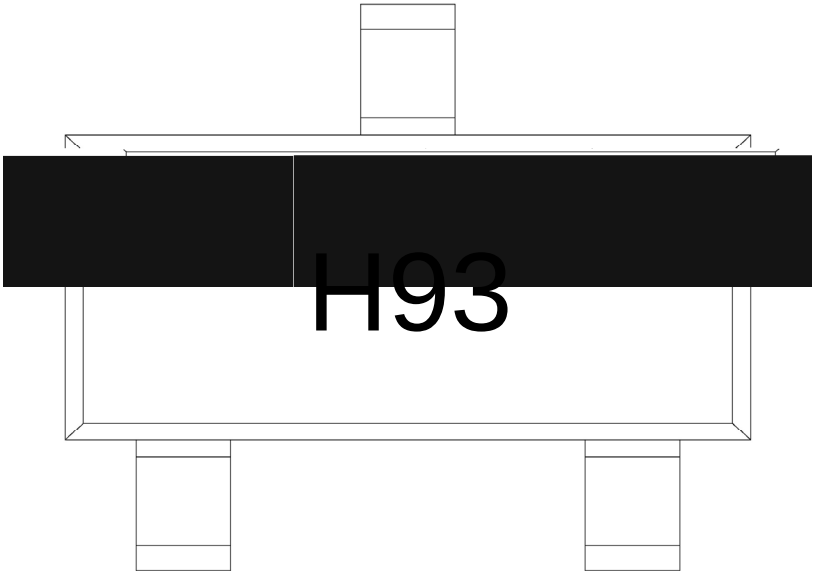
@ Parameter	Z Symbol	Rating	% y Unit
Collector-Base Voltage	V _{CBO}	-50	V
Collector-Emitter Voltage	V _{CEO}	-50	V
Emitter-Base Voltage	V _{EB0}	-5	V
Collector Current	I _C	-100	mA

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Electrical Characteristic Curve



, M y f / Marking Instructions



“ ¢ y
. y , [W A
93 y ” Z W A
Note:
. y Company Code
93 y Product Type Code



šWD t... •Ž¢ (x/) / :KSVKXGZ[XK 6XULORK LUX /8 8KLRU] 9URJKXOTM 6

“¢ y

1o ~‰ 150 %180 - k 60 %90sec;

2o Q 245 r5 - k 4 Ò 5 r0.5sec;

3o D N òi Ò , 2 %10 - /sec.

Note:

1.Preheating:150~180 - , Time:60~90sec.

2.Peak Temp.:245 r5 - , Duration:5 r0.5sec.

3. Cooling Speed: 2~10 - /sec.

ÂD /Cã p ¯ »] / Resistance to Soldering Heat Test Conditions

y 260 r5 -

y 10 r1 sec.

Temp.:260±5

Time:10±1 sec

G P á / Packaging SPEC.

2 & x / REEL

Package Type
7>û ~ E

Units ;>û !H

Dimension ;>û p . (unit Åmm³)